


**PRODUCT / PROCESS CHANGE NOTIFICATION**

**1. PCN basic data**

<b>1.1 Company</b>		STMicroelectronics International N.V
<b>1.2 PCN No.</b>	AMG/18/10427	
<b>1.3 Title of PCN</b>	Product Change Notification on S2-LPQTR product	
<b>1.4 Product Category</b>	S2-LP	
<b>1.5 Issue date</b>	2018-03-16	

**2. PCN Team**

<b>2.1 Contact supplier</b>	
<b>2.1.1 Name</b>	MARCELI SMIALKOWSKI
<b>2.1.2 Phone</b>	+44 1628896277
<b>2.1.3 Email</b>	marceli.smialkowski@st.com
<b>2.2 Change responsibility</b>	
<b>2.2.1 Product Manager</b>	Maria Rosa BORGHI
<b>2.1.2 Marketing Manager</b>	Marco VENERI
<b>2.1.3 Quality Manager</b>	Jean-Marc BUGNARD

**3. Change**

<b>3.1 Category</b>	<b>3.2 Type of change</b>	<b>3.3 Manufacturing Location</b>
Die redesign	Mask or mask set change with new die design	Assembly plant : ST Calamba

**4. Description of change**

	Old	New
<b>4.1 Description</b>	1) The digital LDO voltage regulator operating temperature range is -40 up to +85°C.	1) The digital LDO voltage regulator operating temperature range is -40 up to +105°C. 2) SPI line are tristated when CS is not active. 3) Low Power Mode feature. 4) Antenna diversity fully operating. 5) WMBUS postamble polarity and granularity can be changed. 6) Register DEVICE_INFO now return 0x03C1, register PCKT_PSTMBL changed 7) Other minor changes to improve the ST manufacturing FT yield.
<b>4.2 Anticipated Impact on form,fit, function, quality, reliability or processability?</b>	Finished Good change and DEVICE_INFO register value.	

**5. Reason / motivation for change**

<b>5.1 Motivation</b>	Following Divisional commitment towards continuous improvement, the existing S2-LP design has been improved to enhance the performance ( i.e. operating temperature range, SPI, Low Power Mode ) and to improve ST manufacturing FT yield.
<b>5.2 Customer Benefit</b>	QUALITY IMPROVEMENT

**6. Marking of parts / traceability of change**

<b>6.1 Description</b>	Finished Good change and device info register value.
------------------------	--

**7. Timing / schedule**

<b>7.1 Date of qualification results</b>	2017-10-15
<b>7.2 Intended start of delivery</b>	2018-05-25
<b>7.3 Qualification sample available?</b>	Upon Request

<b>8. Qualification / Validation</b>			
--------------------------------------	--	--	--

<b>8.1 Description</b>			
<b>8.2 Qualification report and qualification results</b>	In progress	<b>Issue Date</b>	

<b>9. Attachments (additional documentations)</b>			
---	--	--	--

10427 Public product.pdf			
--------------------------	--	--	--

<b>10. Affected parts</b>			
---------------------------	--	--	--

<b>10. 1 Current</b>		<b>10.2 New (if applicable)</b>
<b>10.1.1 Customer Part No</b>	<b>10.1.2 Supplier Part No</b>	<b>10.1.2 Supplier Part No</b>
	S2-LPQTR	

**IMPORTANT NOTICE – PLEASE READ CAREFULLY**

STMicroelectronics NV and its subsidiaries (“ST”) reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST’s terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers’ products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2016 STMicroelectronics – All rights reserved